

Fig. 1 PRIOR ART

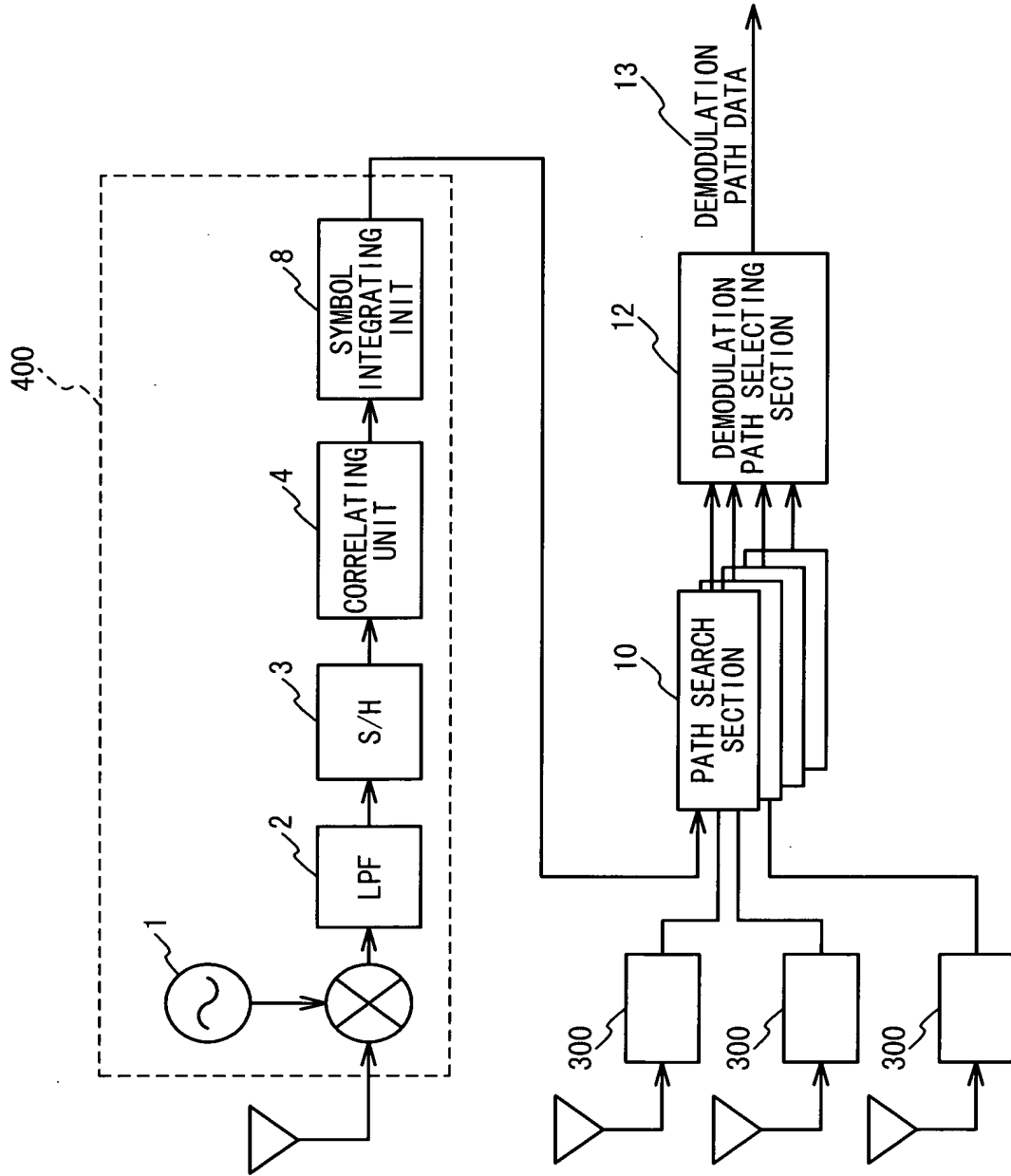


Fig. 2

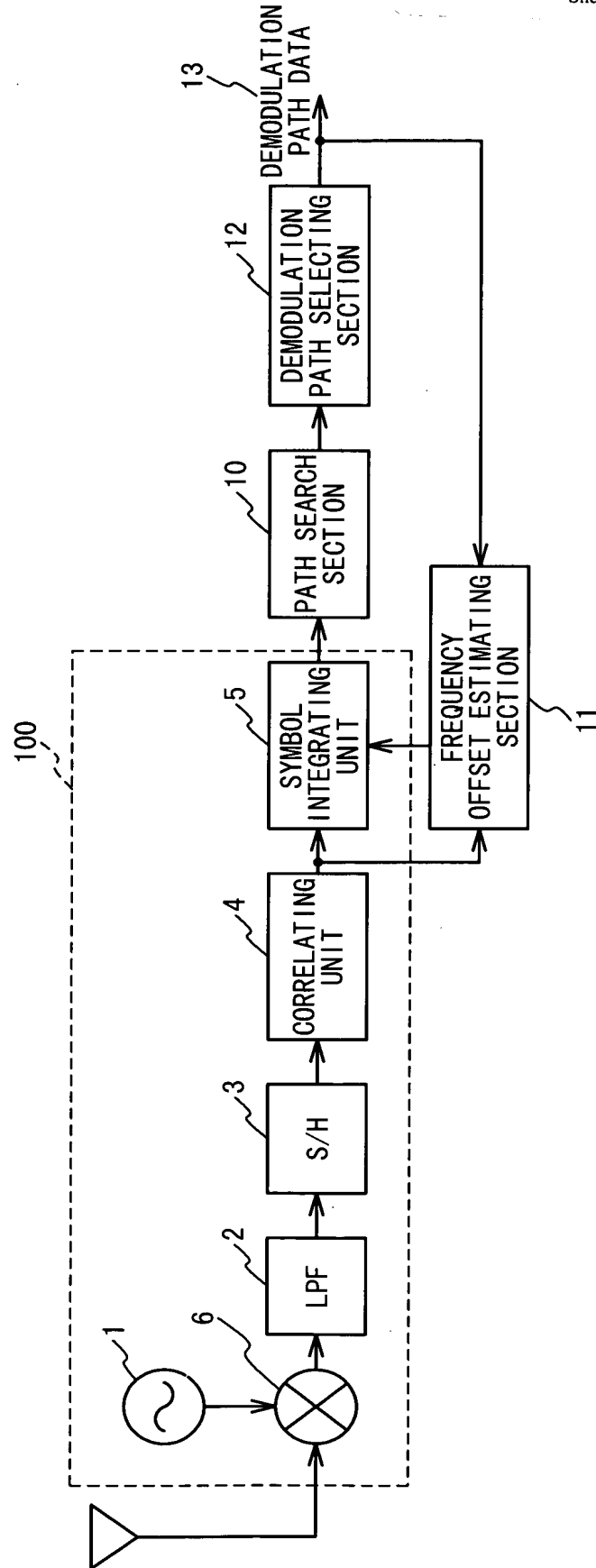
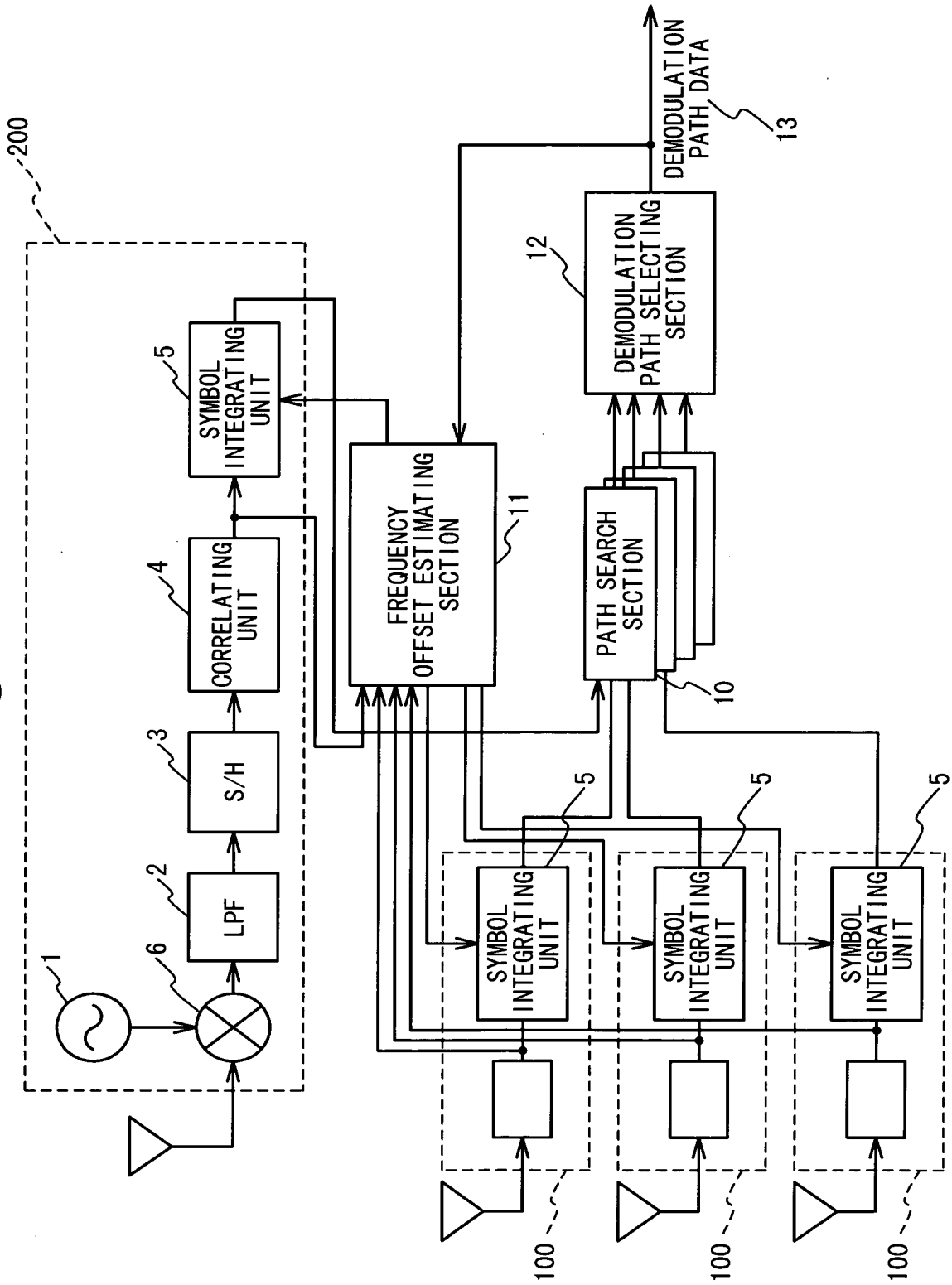


Fig. 3



The diagram illustrates a demodulation system 300. The system is divided into a receiver chain (1-6) and a processing section (7-13). The receiver chain includes an antenna, a multiplier (6), an LPF (2), an S/H (3), a correlating unit (4), and a symbol integrating unit (5). The processing section includes a frequency offset estimating section (11), a path search section (10), a demodulation path selecting section (12), and a slot integrating unit (7). The system also features three parallel branches, each with a multiplier (200) and a symbol integrating unit (5). The output is demodulation path data (13).